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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
10/629,302	07/28/2003	Lewis B. Aronson	15436.247.2.1.4	6438	
22913 WORKMAN N	22913 7590 01/25/2008 WORKMAN NYDEGGER			EXAMINER	
60 EAST SOU	TH TEMPLE		TRAN, DZUNG D		
	GATE TOWER CITY, UT 84111		ART UNIT	PAPER NUMBER	
			2613		
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			MAIL DATE	DELIVERY MODE	
	•		01/25/2008	PAPER	

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

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	Application No.	Applicant(s)				
Office Action Commons	10/629,302	ARONSON ET AL				
• Office Action Summary	Examiner	Art Unit				
	Dzung D. Tran	2613				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 1 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.  - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.  - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).						
Status						
1) Responsive to communication(s) filed on 10 De	ecember 2007					
<u> </u>	action is non-final.					
·=	<u> </u>					
closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims	•					
4)⊠ Claim(s) <u>1-26</u> is/are pending in the application.						
4a) Of the above claim(s) is/are withdrawn from consideration.						
5) Claim(s) is/are allowed.						
6) Claim(s) is/are rejected.	•					
7) Claim(s) is/are objected to.						
8) Claim(s) 1-26 are subject to restriction and/or e	election requirement.					
Application Papers						
9) The specification is objected to by the Examine	r.					
10) The drawing(s) filed on is/are: a) acce		Examiner.				
Applicant may not request that any objection to the						
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).						
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).						
a) All b) Some * c) None of:						
1. Certified copies of the priority documents have been received.						
<ul> <li>2. Certified copies of the priority documents have been received in Application No</li> <li>3. Copies of the certified copies of the priority documents have been received in this National Stage</li> </ul>						
application from the International Bureau (PCT Rule 17.2(a)).						
* See the attached detailed Office action for a list of the certified copies not received.						
Attachment(c)						
Attachment(s)  1) Notice of References Cited (PTO-892)	4) Interview Summary	(PTO-413)				
2) D Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Da	nte				
) Information Disclosure Statement(s) (PTO/SB/08)  Paper No(s)/Mail Date  5) Notice of Informal Patent Application 6) Other:						
Paper No(s)/Mail Date	o) [_] Oller					

Application/Control Number:

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## Election/Restrictions

- 1. Restriction to one of the following inventions is required under 35 U.S.C. 121:
- I. Claims 1-12, drawn to a method/apparatus of an integrated circuit for use in a transceiver module, the integrated circuit comprises a loopback circuitry for switchably forming a loopback data path, classified in class 398, subclass 137.
- II. Claims 13-26, drawn to a method/apparatus of an integrated circuit for use in a transceiver module, the integrated circuit comprising: a bit error rate tester (BERT) engine for testing a test data path from a starting test point to an ending test point, classified in class 398, subclass 32.
- 2. The inventions are distinct, each from other because of the following reasons:

Inventions I, II are unrelated. Inventions are unrelated if it can be shown that they are not disclosed as capable of use together and they have different modes of operation, different functions, or different effects (MPEP § 806.04, MPEP § 808.01). In the instant case the different inventions are unrelated because invention I is related to the integrated circuit comprises a loopback circuitry for switchably forming a loopback data path. Invetion II related to the integrated circuit comprising: a bit error rate tester (BERT) engine for testing a test data path from a starting test point to an ending test

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point. The invention I and II different because invention I does not have a bit error rate tester (BERT) engine for testing a test data path and invention I does not have a loopback circuitry for switchably forming a loopback data path.

- 3. Because these inventions are distinct for the reasons given above and have acquired a separate status in the art as shown by their different classification, restriction for examination purposes as indicated is proper.
- 4. Because these inventions are distinct for the reasons given above and the search required for Group I is not required for Group II, restriction for examination purposes as indicated is proper.
- 5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dzung D Tran whose telephone number is (571) 272-3025. The examiner can normally be reached on 9:00 AM - 7:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jason Chan can be reached on (571) 272-3022. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

DT

01/21/2008

DZUNG TRAN

PRIMARY PATENT EXAMINER